

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Term	Documents
BOUND.USPT.	162720
BOUNDS.USPT.	23981
(45 SAME BOUND).USPT.	0
(L45 SAME BOUND).USPT.	0

Database:

US Patents Full-Text Database
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 IBM Technical Disclosure Bulletins

Search:

L46

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side by side

Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=ADJ

<u>L46</u>	L45 same bound	0	<u>L46</u>
<u>L45</u>	L44 same l25	12	<u>L45</u>
<u>L44</u>	number adj4 (page or block) adj4 segment	337	<u>L44</u>
<u>L43</u>	object reference same bound field	0	<u>L43</u>
<u>L42</u>	L38 same bound field	18	<u>L42</u>
<u>L41</u>	L38 same bound filed	0	<u>L41</u>
<u>L40</u>	L38 same object reference	19208	<u>L40</u>
<u>L39</u>	l38 same bound	4931	<u>L39</u>
<u>L38</u>	l17 or object reference	708047	<u>L38</u>

<u>L37</u>	l33 same capability	128	<u>L37</u>
<u>L36</u>	bound or bound field	182053	<u>L36</u>
<u>L35</u>	bound or bound field	182053	<u>L35</u>
<u>L34</u>	l18 and l25	8	<u>L34</u>
<u>L33</u>	process same (protection or protect or security) same (address space or memory or segment)	3700	<u>L33</u>
<u>L32</u>	L31 and l28	6	<u>L32</u>
<u>L31</u>	L30	789	<u>L31</u>
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L30</u>	(protect or protection or protected) same (segment or memory) same process same segment	1058	<u>L30</u>
<u>L29</u>	l28 same (base or base address or offset)	37	<u>L29</u>
<u>L28</u>	L27 same (segment or segment descriptor)	125	<u>L28</u>
<u>L27</u>	l17 same l25	1617	<u>L27</u>
<u>L26</u>	segment same l25	975	<u>L26</u>
<u>L25</u>	(block or page) adj (size or length)	19284	<u>L25</u>
<u>L24</u>	l21 same (permission or flag)	12	<u>L24</u>
<u>L23</u>	l18 same l22	4	<u>L23</u>
<u>L22</u>	(permission or flag) same l17	15477	<u>L22</u>
<u>L21</u>	L20 same segment	135	<u>L21</u>
<u>L20</u>	l17 same l19	1988	<u>L20</u>
<u>L19</u>	block adj2 (size or length or field)	28626	<u>L19</u>
<u>L18</u>	l17 same bound field	19	<u>L18</u>
<u>L17</u>	(descriptor or pointer or capability or handle or token)	1198089	<u>L17</u>
<u>L16</u>	(descriptor or pointer or token or handle) same permission same bound same segment	8	<u>L16</u>
<u>L15</u>	(descriptor or pointer or token or handle) same permission same bound same segment	4	<u>L15</u>
<u>L14</u>	(capability or handle or pointer) same permission same (segment or segment descriptor)	33	<u>L14</u>
<u>L13</u>	capability adj3 (address or addressing or pointer)	1810	<u>L13</u>
<u>L12</u>	(share or sharing) same (process or processes) same (data or memory)	7879	<u>L12</u>
<u>L11</u>	pointer same (block adj4 (size or length or field)) same segment	108	<u>L11</u>
<u>L10</u>	((guarded or capability) adj2 pointer) same length	6	<u>L10</u>
<u>L9</u>	((guarded or capability) adj2 pointer) or pointer) same length	10569	<u>L9</u>
<u>L8</u>	capability same segment	7968	<u>L8</u>
<u>L7</u>	capability same l6	0	<u>L7</u>
<u>L6</u>	(absolute adj2 address) adj4 object	44	<u>L6</u>
<u>L5</u>	(disable or disabling or enable or enabling or cancel or canceling) adj4 l4	15	<u>L5</u>
<u>L4</u>	auto precharge or (auto adj2 (pre adj charge))	251	<u>L4</u>

<u>L3</u>	synchronous DRAM or SDRAM	8813	<u>L3</u>
<u>L2</u>	(auto or automatic or automatically) adj4 ((pre adj charge) or precharge)	453	<u>L2</u>
<u>L1</u>	(schedule or scheduling) same memory same access same (reduce or reducing) same latency	25	<u>L1</u>

END OF SEARCH HISTORY